

CALL FOR PAPERS

The 3rd International Workshop on Embedded Software Optimization (ESO 2008)

<http://www.utdallas.edu/~qiumk/ESO2008/>

in conjunction with The 2008 IEEE/IFIP International Conf. on Embedded and Ubiquitous Computing (EUC 2008)

<http://epcc.sjtu.edu.cn/euc2008/>

December 17 - 20, 2008, Shanghai, China

General Co-Chairs

Edwin H.-M. Sha, University of Texas at Dallas, USA
Alex Orailoglu, University of California, San Diego, USA

Program Co-Chairs

Meikang Qiu, University of New Orleans, USA
Chun Jason Xue, City University of Hong Kong, Hong Kong

Program Committee

Ben A. Abderazek, University of Aizu, Japan
Xing Cai, University of Oslo, Norway
Steven M. Carr, Michigan Technological University, USA
Chantana Chantrapornchai, Silpakorn University, Thailand
Sao-Jie Chen, National Taiwan University, Taiwan
Ying Chen, Shanghai Jiaotong University China
Albert Cheng, University of Houston, USA
Yeh-Ching Chung, National TsingHua University, Hsinchu, Taiwan
Lan-Rong Dung, National Chiao Tung University, Taiwan
Stephen A. Edwards, Columbia University, USA
Hani A K Hagrass, University of Essex, UK
Pao-Ann Hsiung, National Chung Cheng University, Taiwan
Wen-Jyi Hwang, National Taiwan Normal University, Taiwan
Zhiping Jia, Shandong University, China
Kazuki Joe, Nara Woman's University, Japan
Seon Wook Kim, Korea University, Korea
Tei-wei Kuo, National Taiwan University, Taiwan
Yu-Kwong Kwok, University of Hong Kong, China
Jenq-Kuen Lee, National Tsinghua University, Taiwan
Yann-Hang Lee, Arizona State University, USA
Xuandong Li, Nanjing University, China
Jie Li, University of Tsukuba, Japan
Yingshu Li, Georgia State University, USA
Man Lin, St. Francis Xavier University, Canada
Meilin Liu, Wright State University, USA
Sanglu Lu, Nanjing University, China
Alberto Macii, Politecnico di Torino, Italy
Rabi N. Mahapatra, Texas A&M University, USA
Tulika Mitra, National Singapore University, Singapore
Jogesh Muppala, Hong Kong University of Sci. and Tech., Hong Kong
Koji Nakano, Hiroshima University, Japan
Nicolas Navet, LORIA, France
Timothy O'Neil, University of Akron, USA
Andy Pimentel, University of Amsterdam, Netherland
Nan Sang, University of Electronic Science and Technology China
Wei-Kuan Shih, National TsingHua University, Taiwan
Phil Sweany, University of North Texas, USA
Jarmo Takala, Tampere University of Technology, Finland
Hiroyuki Tomiyama, Nagoya University, Japan
Sissades Tongsimma, National Center for Genetic Eng. and Bio., Thailand
Lorenzo Verdoscia, ICAR National Research Council (CNR), Italy
Salvatore Vitabile, University of Palermo, Italy
Guojung Wang, Central South University, China
Cho-Li Wang, The University of Hong Kong, Hong Kong
Ting-Chi Wang, National TsingHua University, Taiwan
Hongxing Wei, Beihang University, China
Gang Wu, Shanghai Jiaotong University, China
Jiang Xu, Hong Kong University of Science and Technology, Hong Kong
Youtao Zhang, University of Pittsburgh, USA
Huiyang Zhou, University of Central Florida, USA
Dakai Zhu, University of Texas at San Antonio, USA
Qiaoming Zhu, Suzhou University, China
(Check the workshop web page for the current list.)

Scope and Topics of Interest

Embedded systems are driving an information revolution with their pervasion in our everyday lives. The increasingly ubiquitous embedded systems pose a host of technical challenges different from those faced by general-purpose computers because they are more constrained in terms of timing, power, area, memory and other resources. Embedded software optimization becomes a major concern for embedded system design.

The objective of ESO 2008 is to provide a forum for scientists, engineers, and researchers to discuss and exchange their new ideas, novel results, work in progress and experience on all aspects of software optimization for embedded and ubiquitous systems. Topics of particular interest include, but are not limited to:

- Software optimization techniques for power, timing, memory, cost minimization, reliability and security
- Code size optimization techniques
- Software design and optimization for multi-core, parallel, distributed or heterogeneous embedded and ubiquitous systems
- Software management for memory systems
- Embedded networks and sensor network optimizations
- Real-time software design and optimization
- Security and fault tolerance for embedded or ubiquitous systems
- Application-specific embedded software design and optimization
- Code generation and optimization for embedded processors
- Compilation for reconfigurable architectures
- Design space exploration and minimization
- Embedded Java optimization
- Profiling and analysis techniques for embedded software
- Verification and testing techniques for embedded software
- Power/energy estimation tools for embedded software
- Case studies for various applications

Important Dates

Paper submission due	: 15 June, 2008
Acceptance notification	: 18 August, 2008
Camera-ready due	: 22 September, 2008
Author registration	: 22 September, 2008
Conference	: 17-20 December, 2008

Submission Information

Submissions should include abstract, 5-10 keywords, and the e-mail address of the corresponding author and be in PDF format. Each submission must not exceed 6 pages in the IEEE 8.5" x 11" two-column format with 10-12 point font (Template: DOC or LaTeX), including tables and figures. Each accepted paper needs at least one author to register in the conference. Each submission should be regarded as an undertaking that, should the submission be accepted, at least one of the authors must attend the conference to present the work in order that the accepted paper can be put into digital library. The final version of an accepted paper will be limited to 6 pages in IEEE proceeding format. The workshop paper submission can be done through system <http://cse.stfx.ca/~euc08workshops/sub/>

The accepted papers have to be presented orally at the conference and will be published by IEEE CS as one separate proceeding (indexed by EI). The selected best papers will be published in special issues of journals, including International Journal of Parallel, Emergent and Distributed Systems, and Journal of Embedded Computing.